

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,155,649 B2

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APPLICATION NO. : 10/722752

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INVENTOR(S) : Toshinobu Nakao, Shinji Ozaki and Tomohisa Sezaki

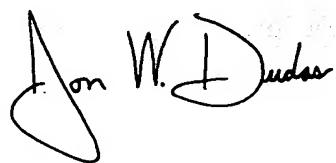
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 13, Line 36. The phrase "output signal "1" " is incorrect and should be replaced with --output signal "0"--.

In the Claims, relating to Claim 9, Column 23, Line 29. The phrase "n-th" is incorrect and should be replaced with --n-1th--.

Signed and Sealed this

Thirteenth Day of March, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office